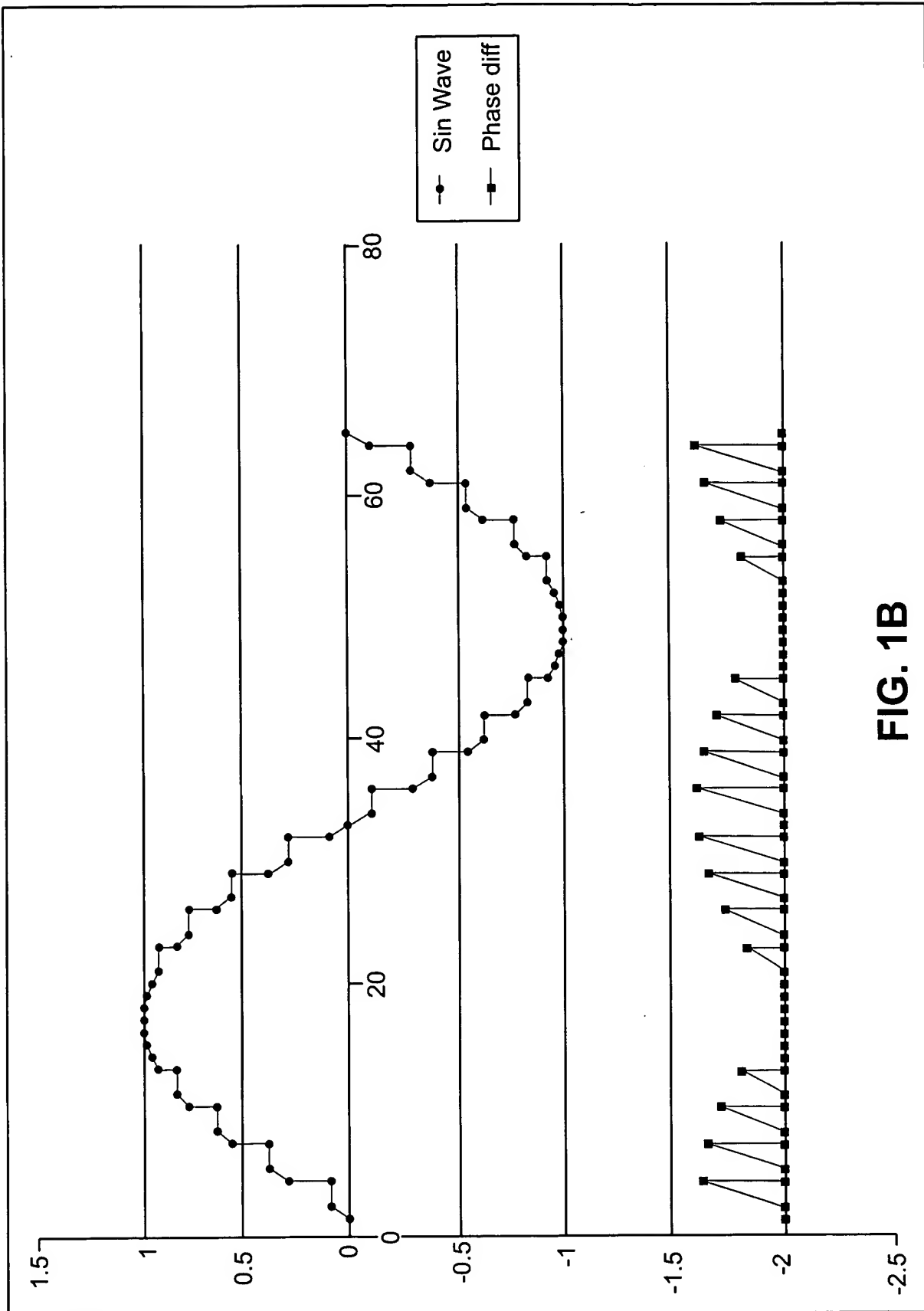


FIG. 1A



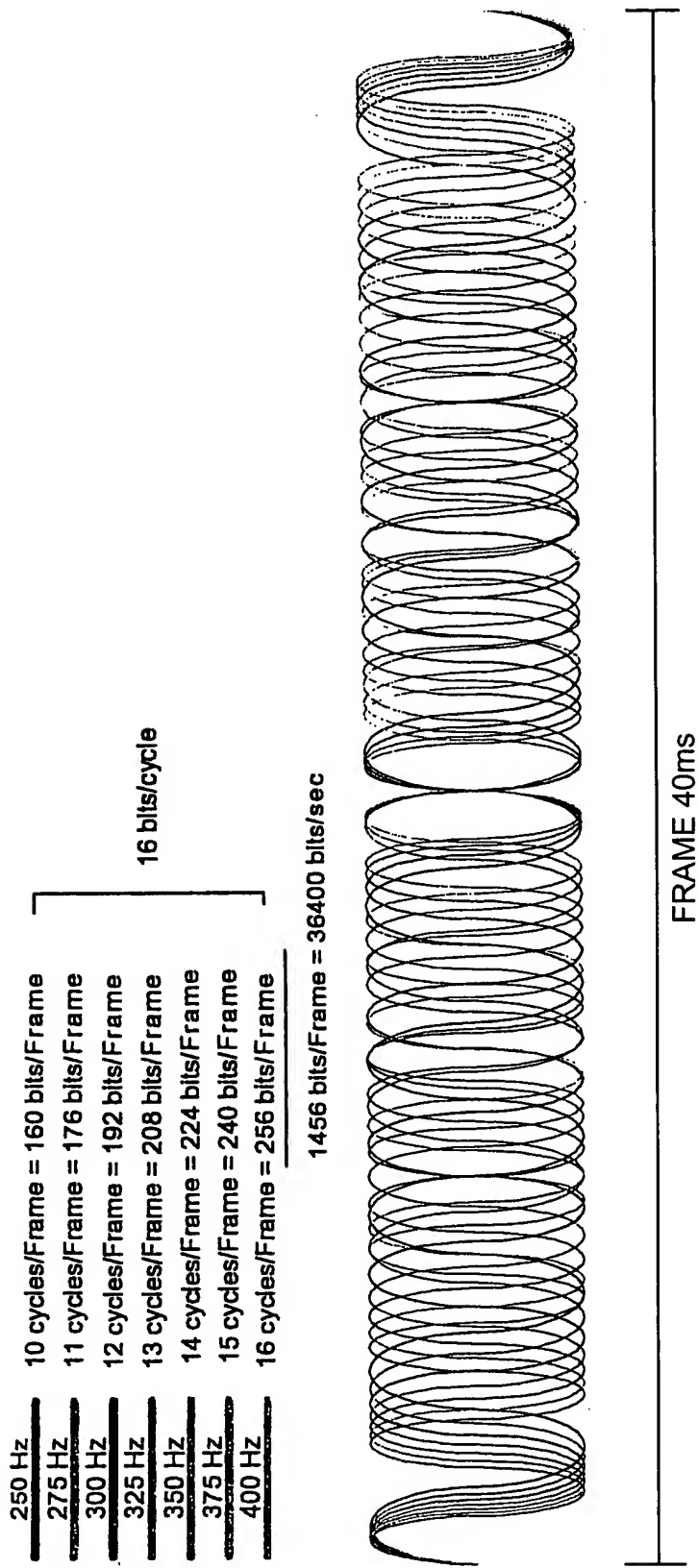


FIG. 2

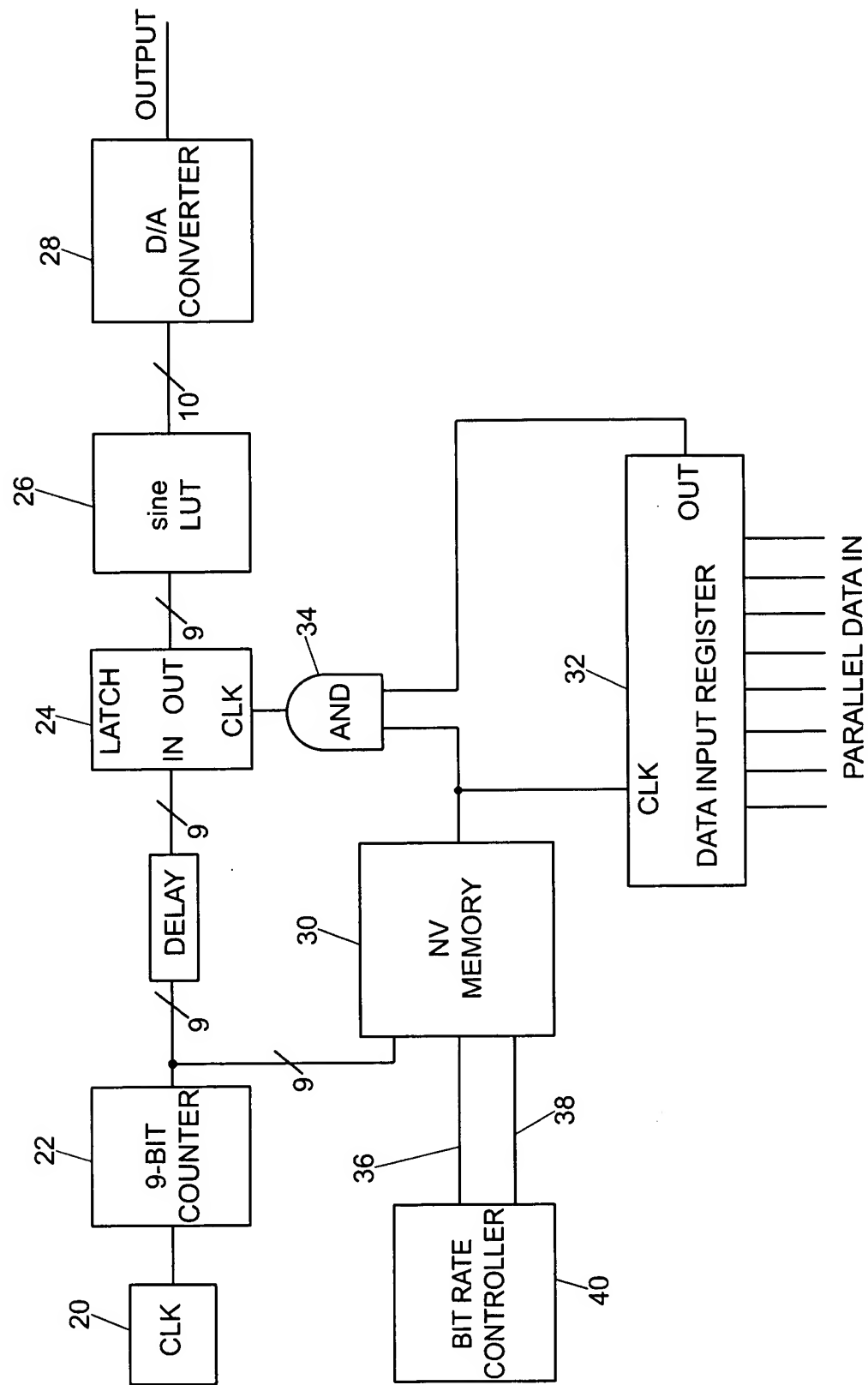
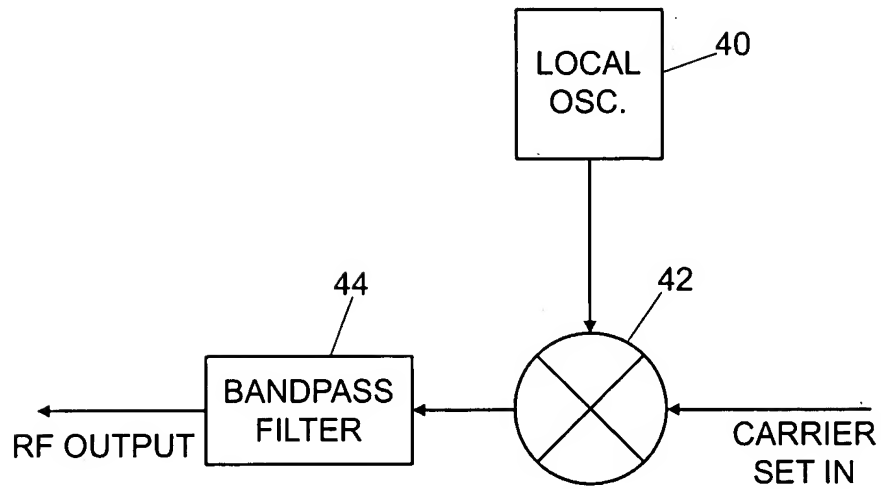
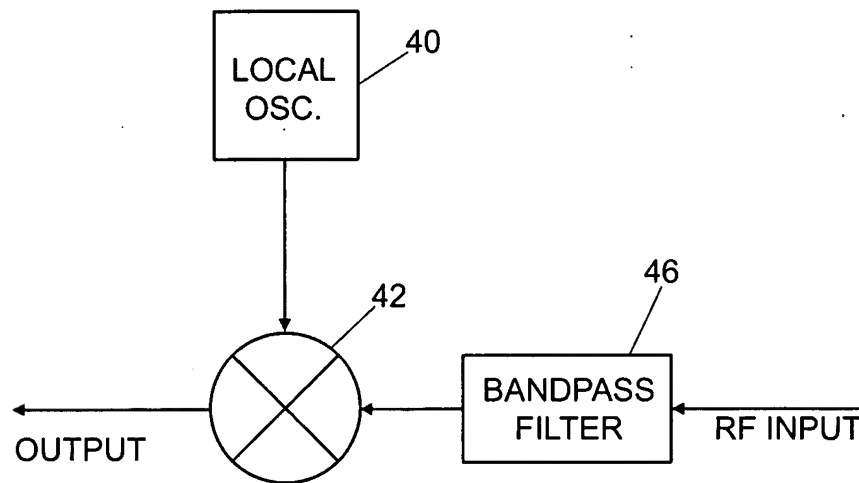


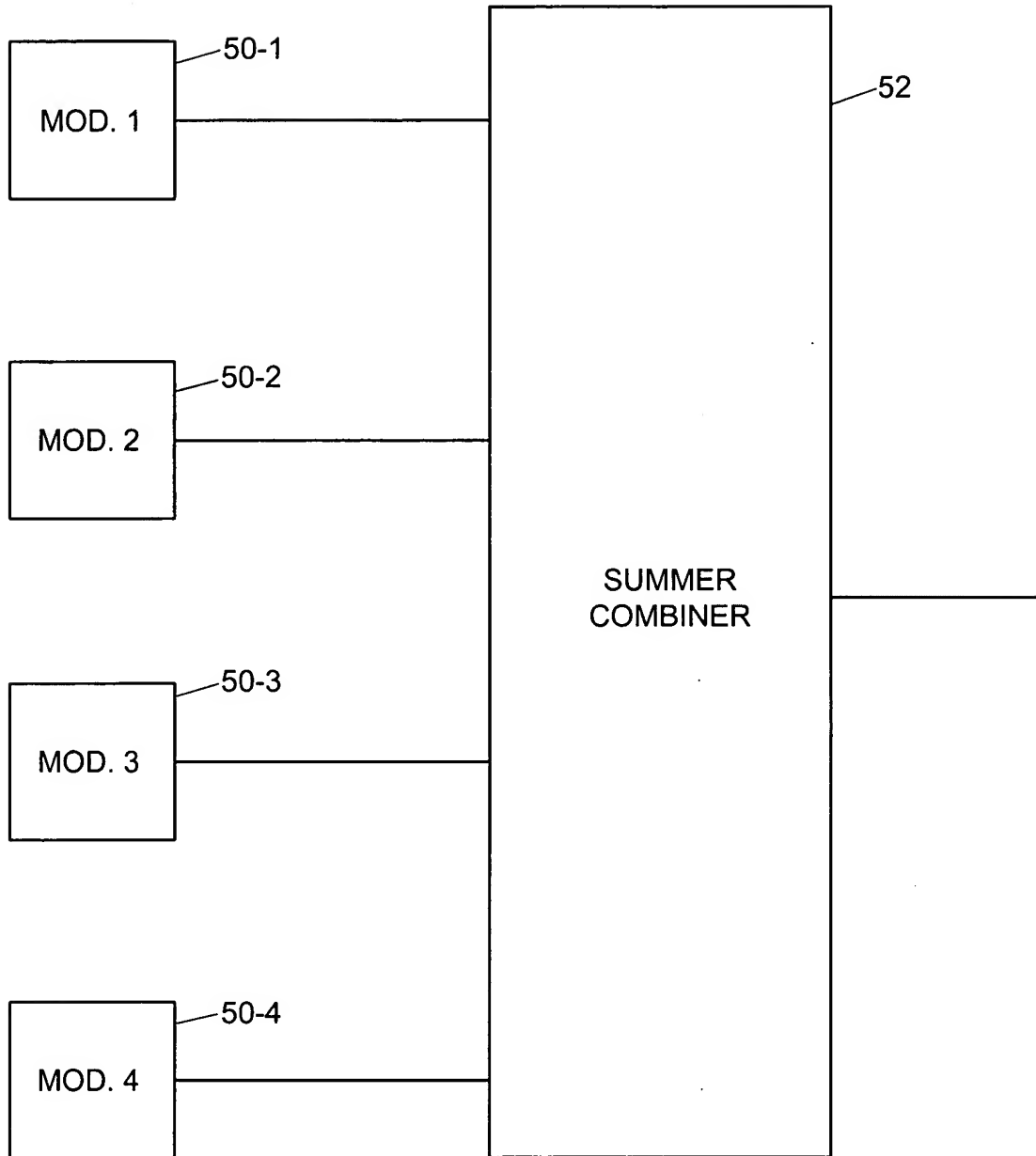
FIG. 3



**FIG. 4A**



**FIG. 4B**



**FIG. 5A**

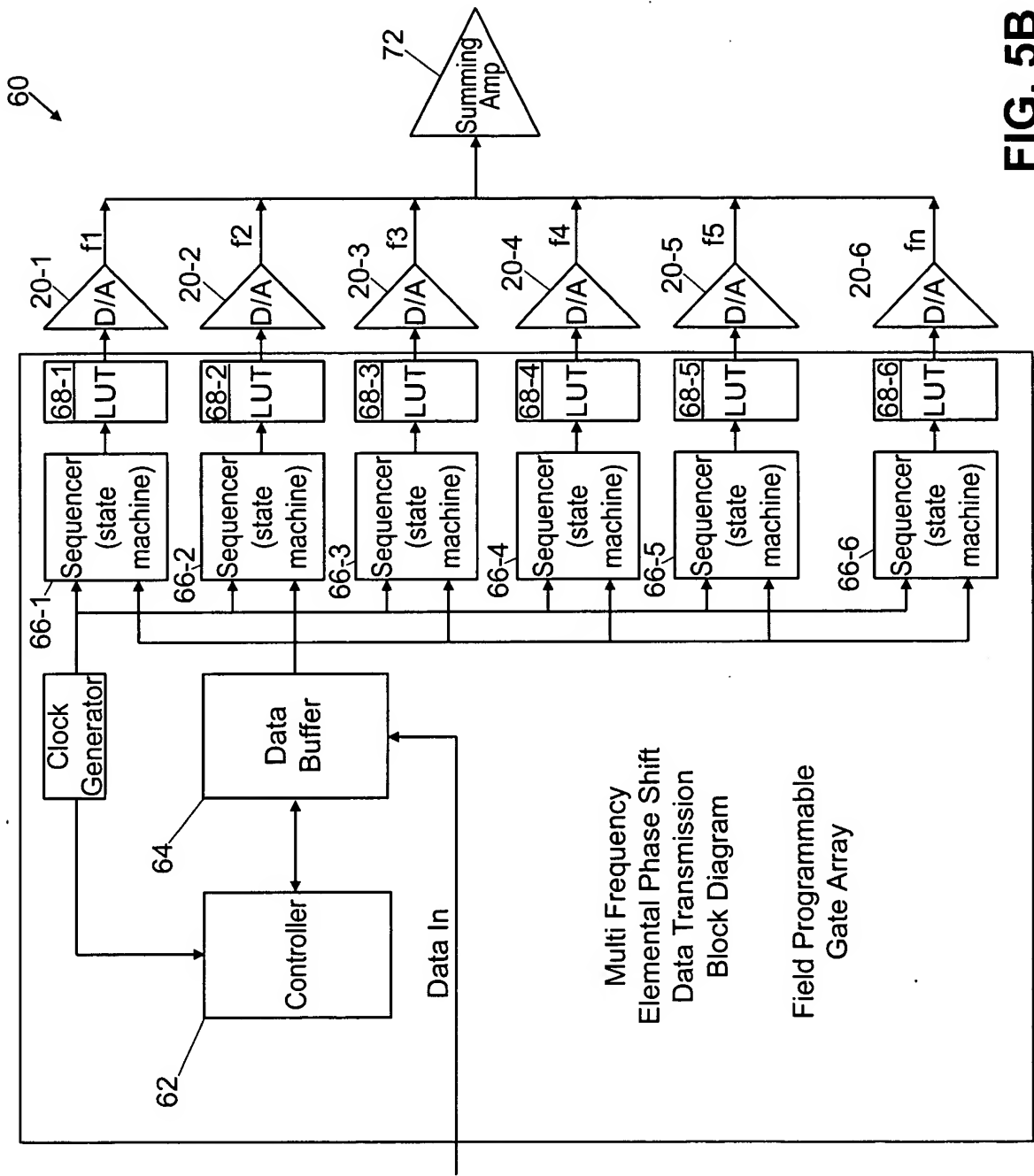
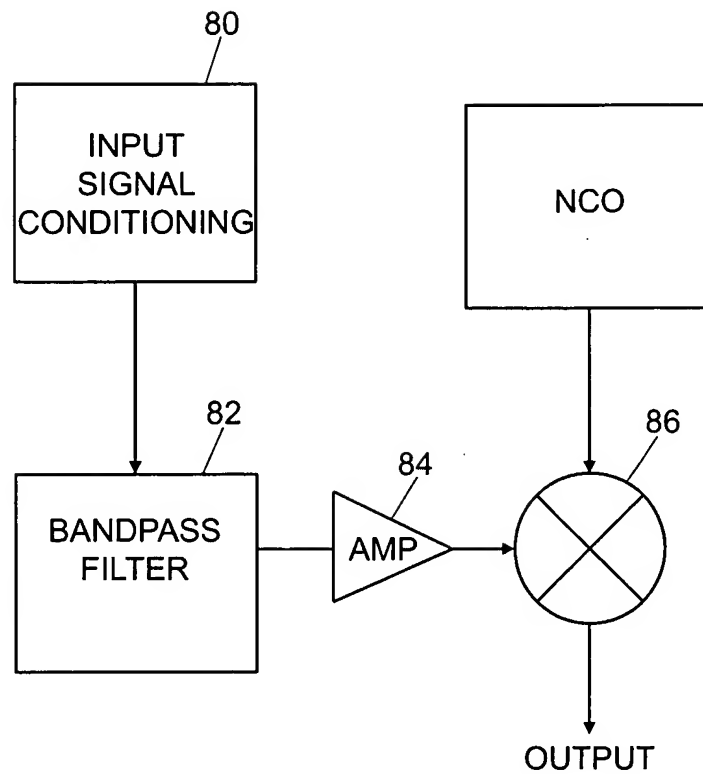


FIG. 5B



**FIG. 6**



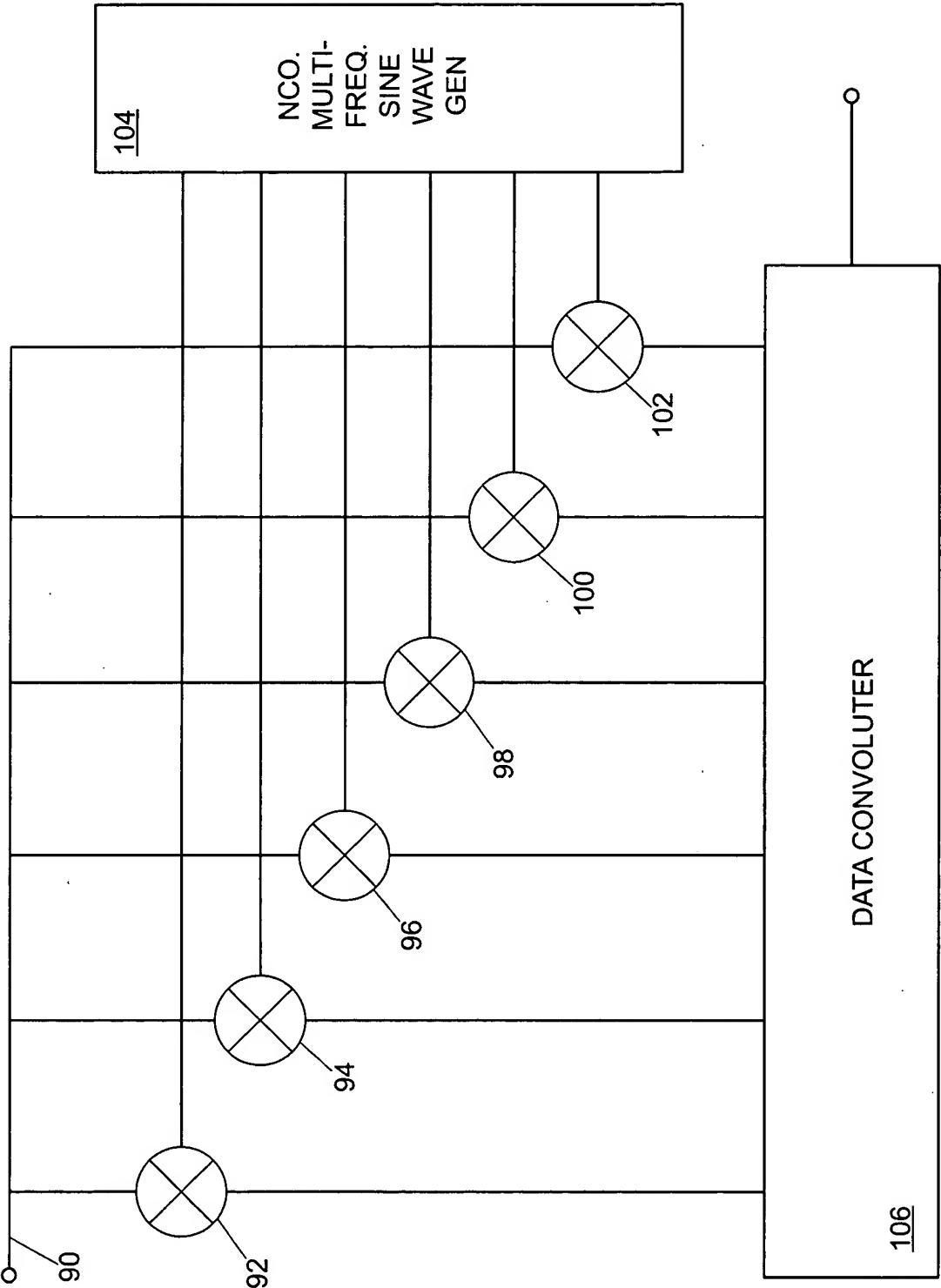
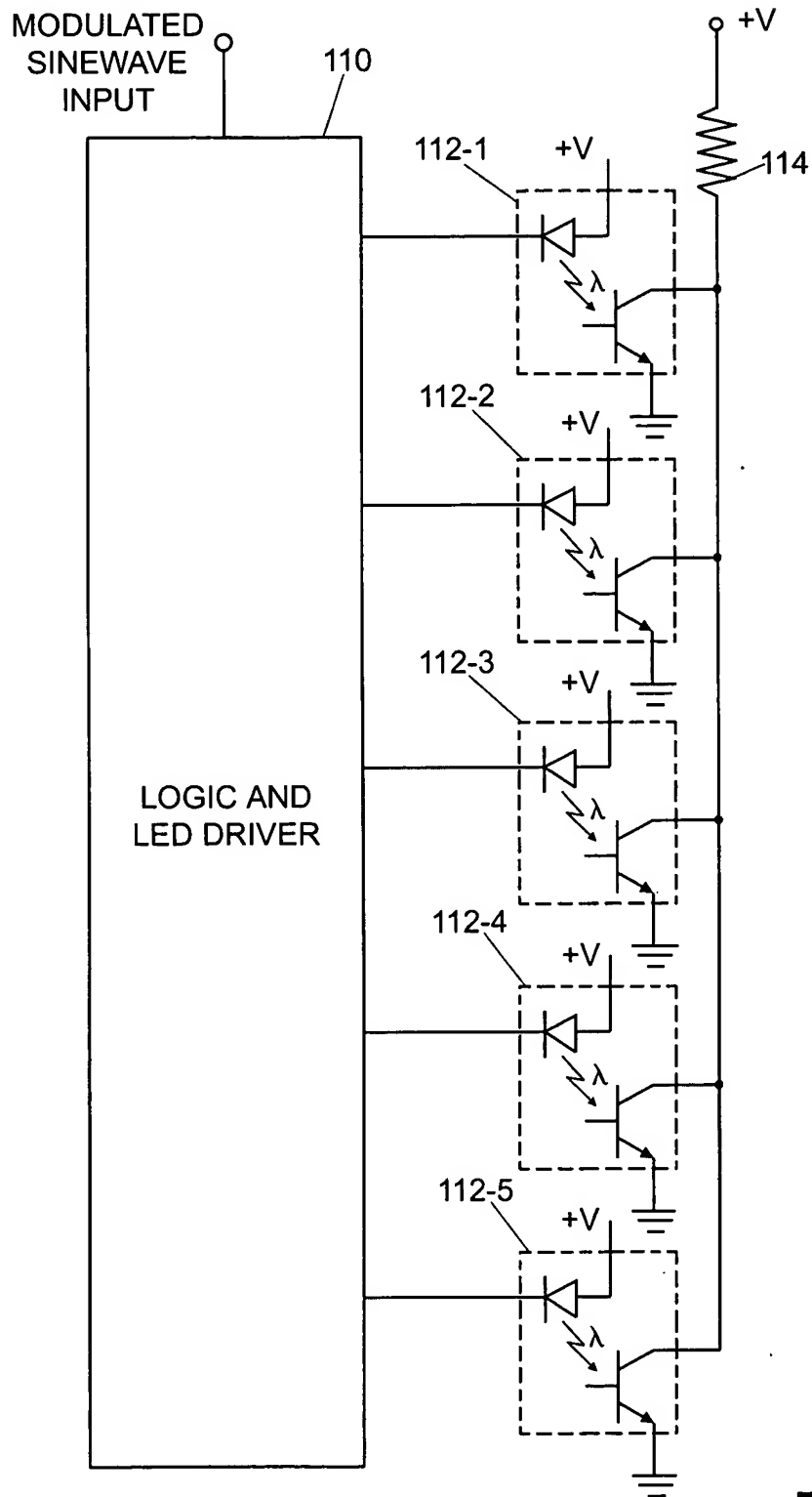
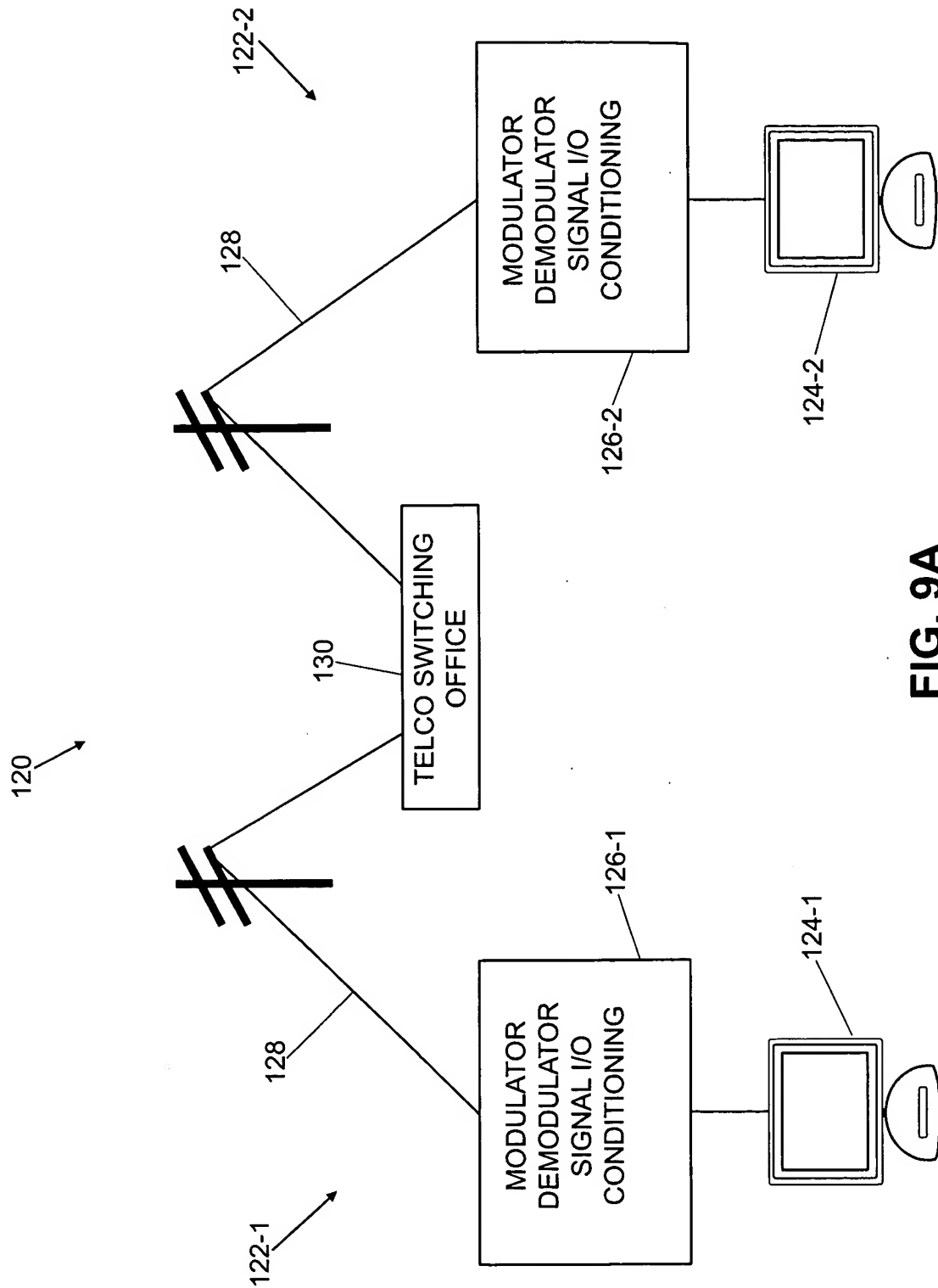


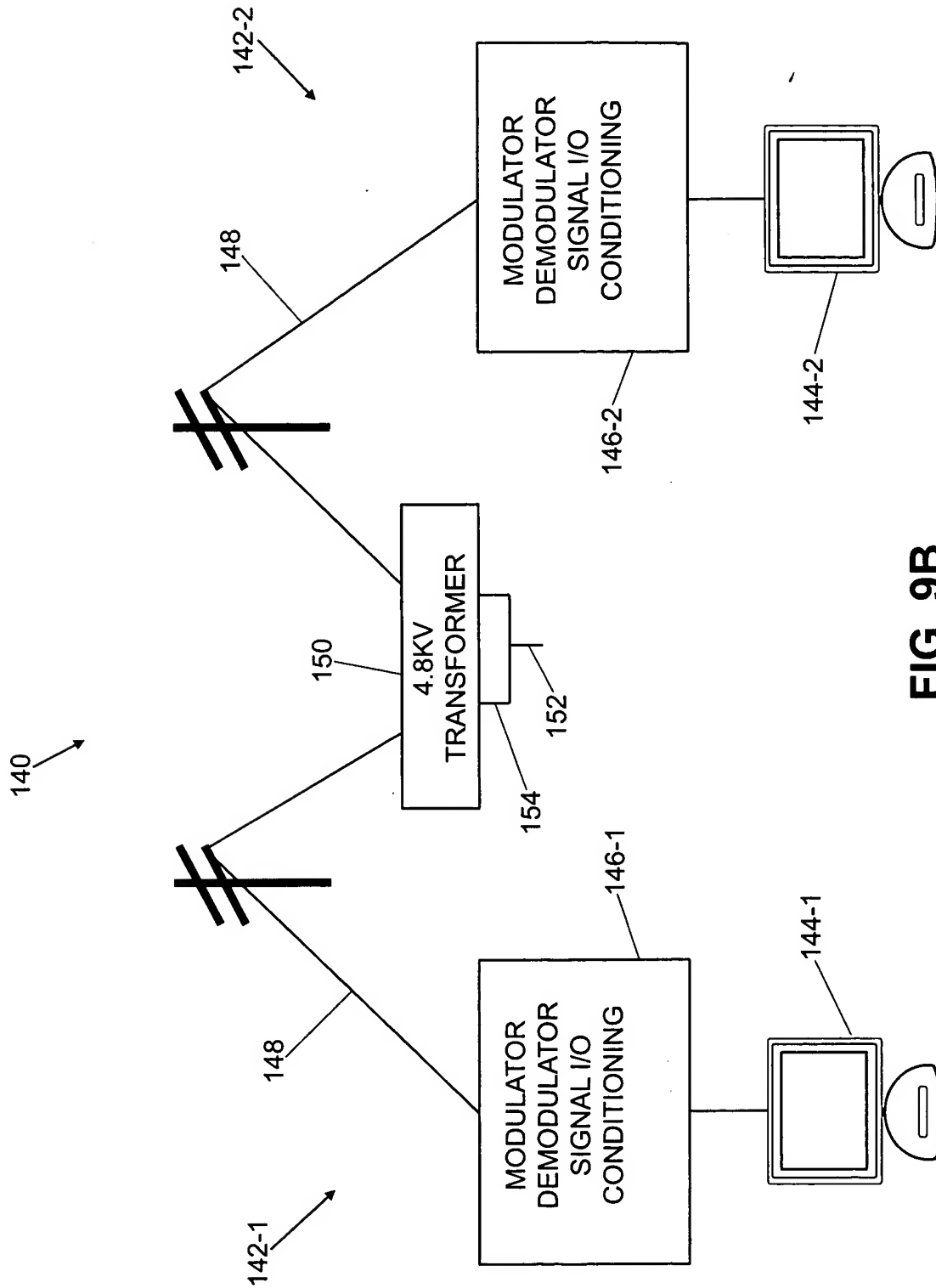
FIG. 7



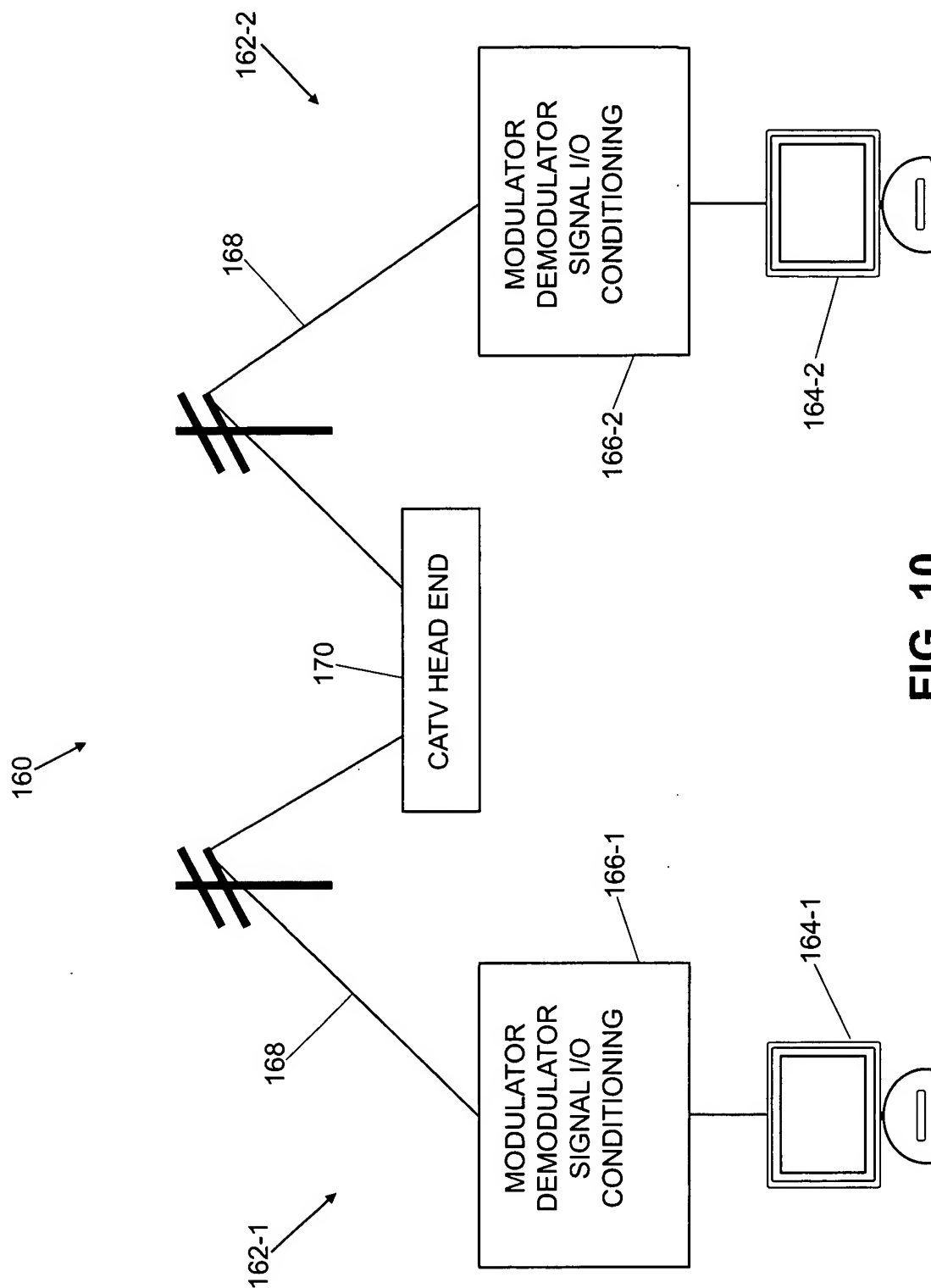
**FIG. 8**



**FIG. 9A**



**FIG. 9B**



**FIG. 10**

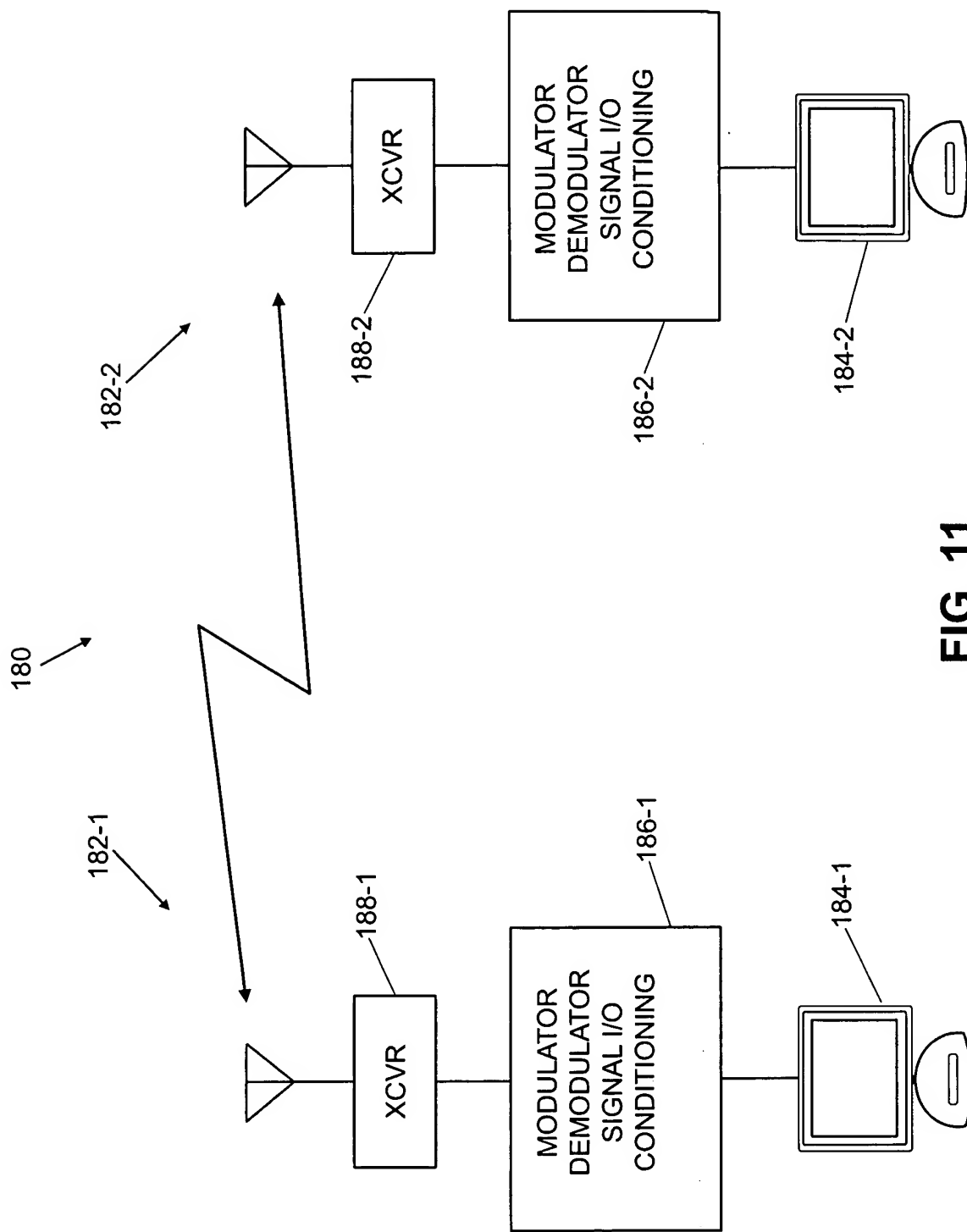
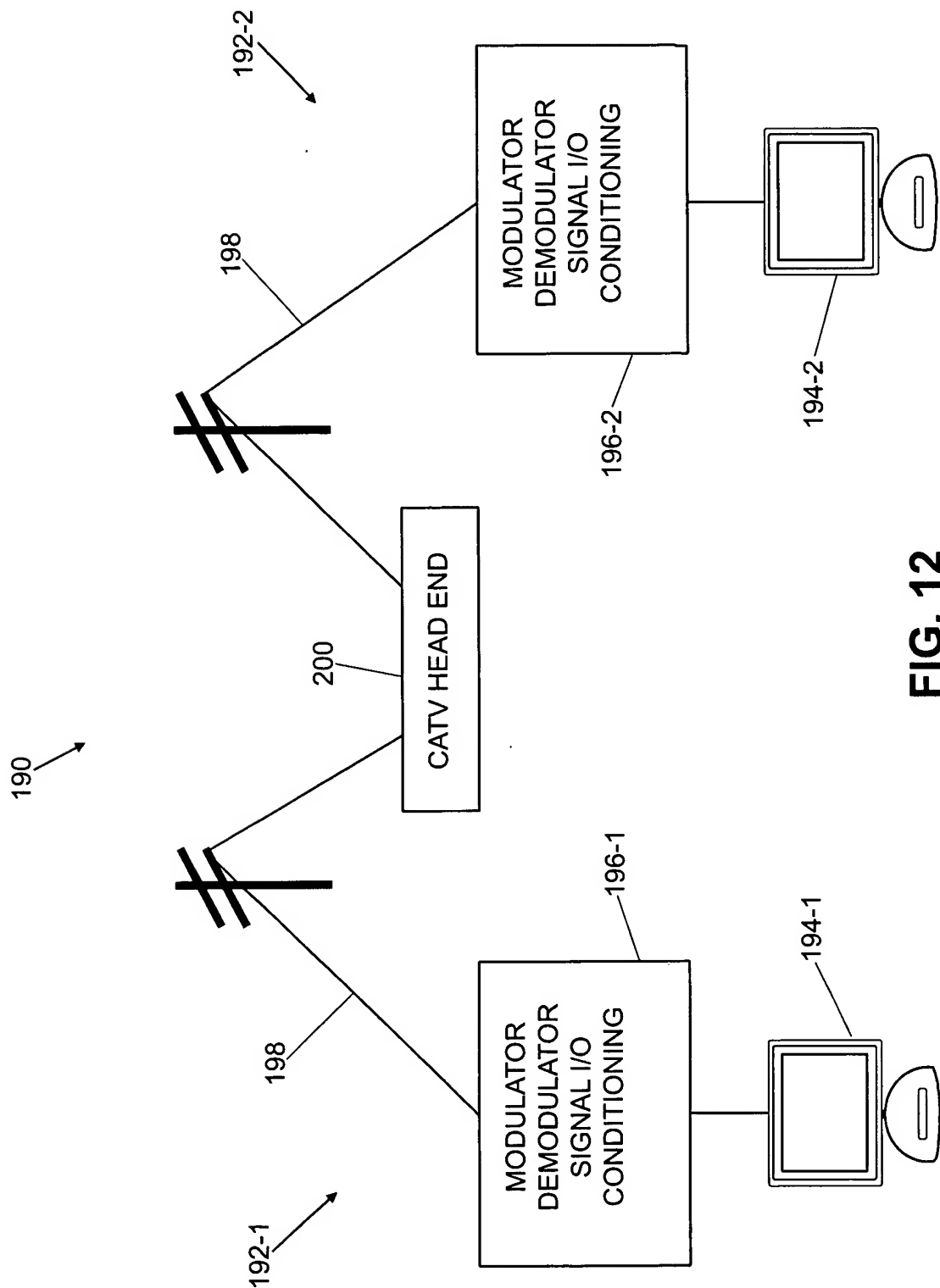
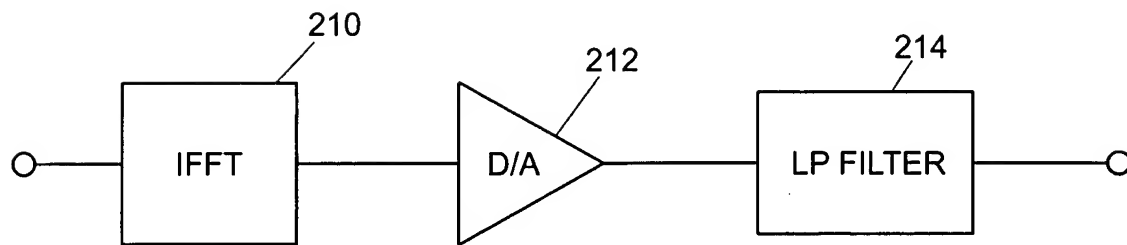


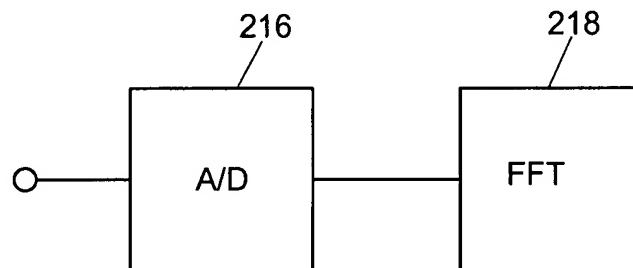
FIG. 11



**FIG. 12**

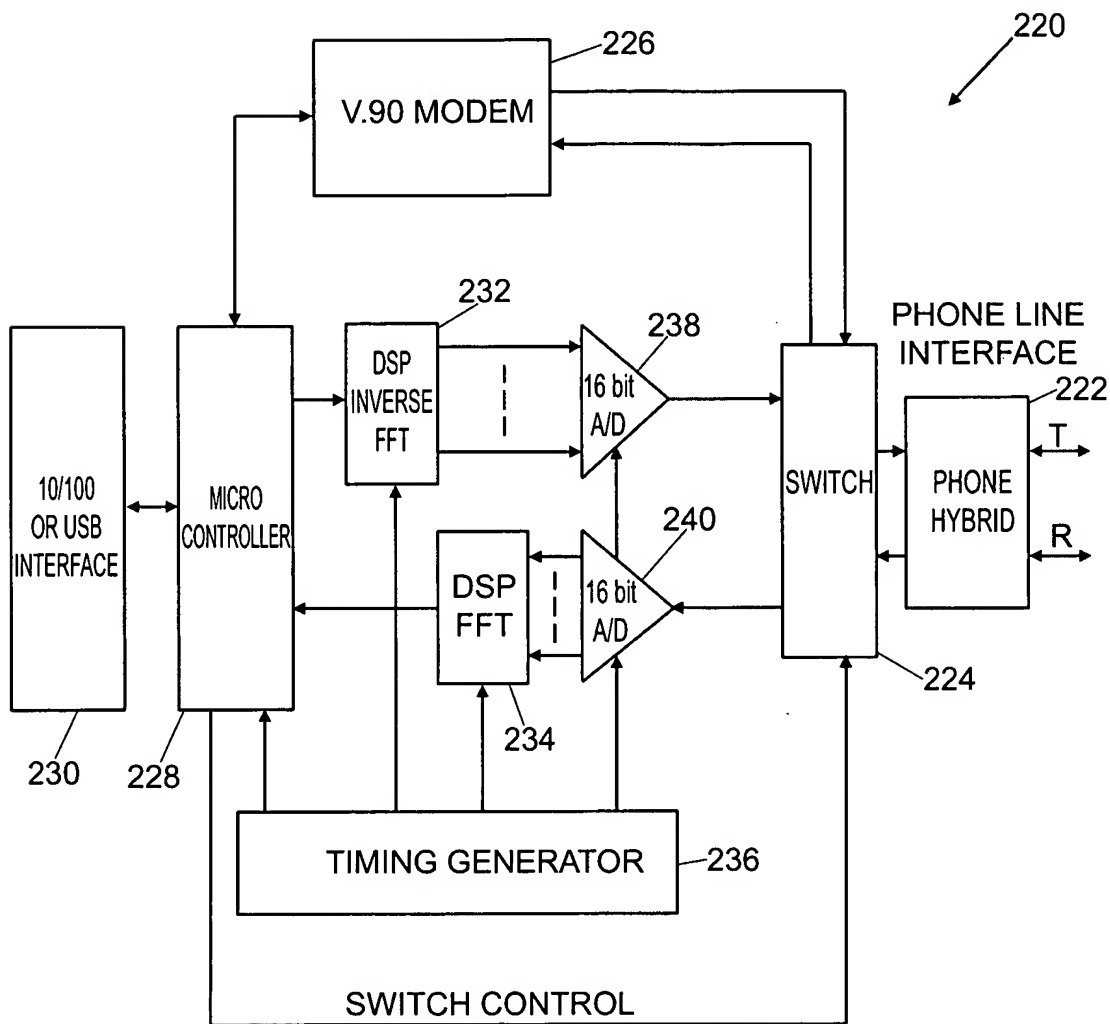


**FIG. 13**

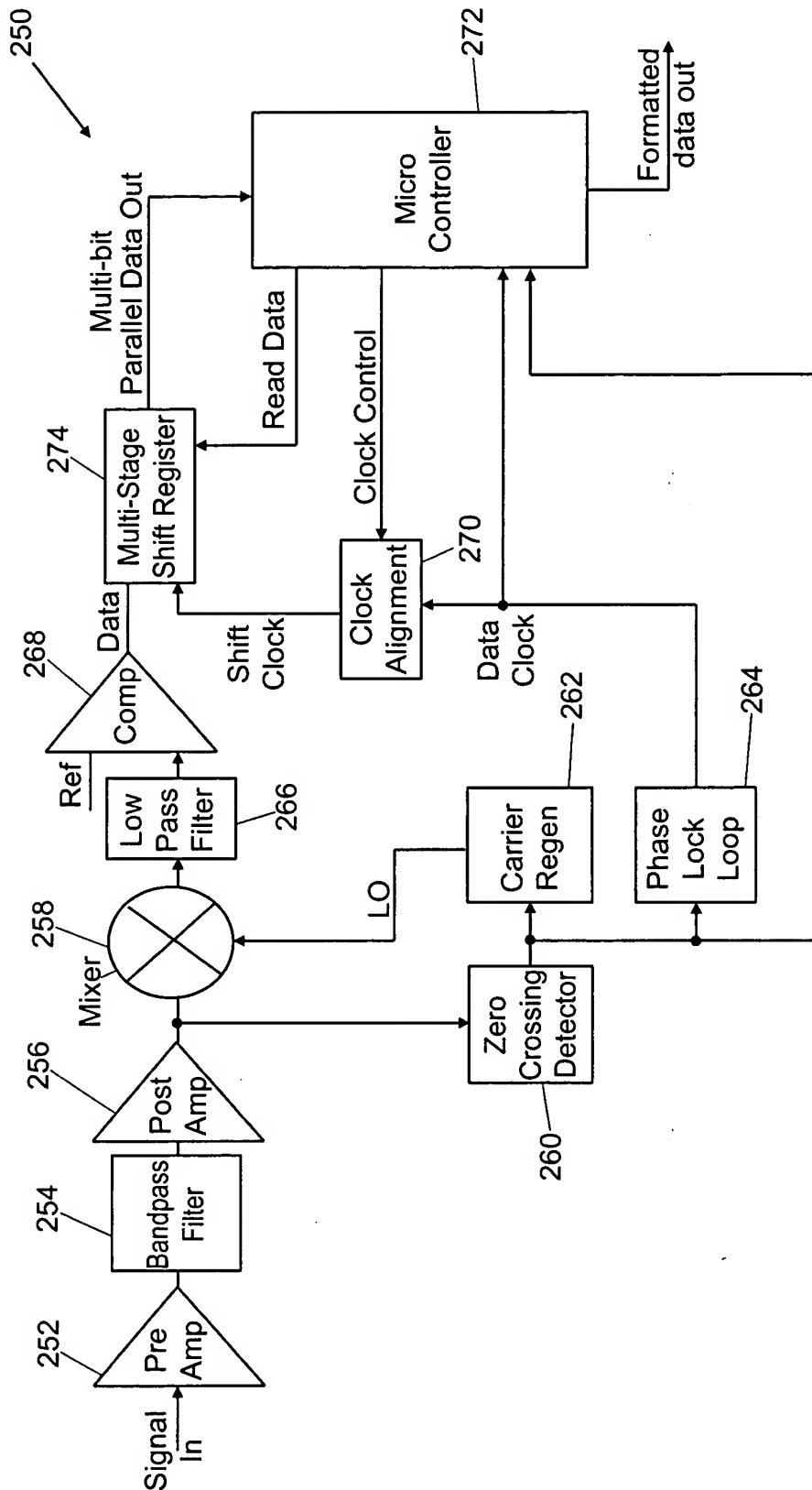


**FIG. 14**





**FIG. 15**



Multi-bit Sinewave  
Receiver  
Block Diagram

**FIG. 16**